

Figure 2

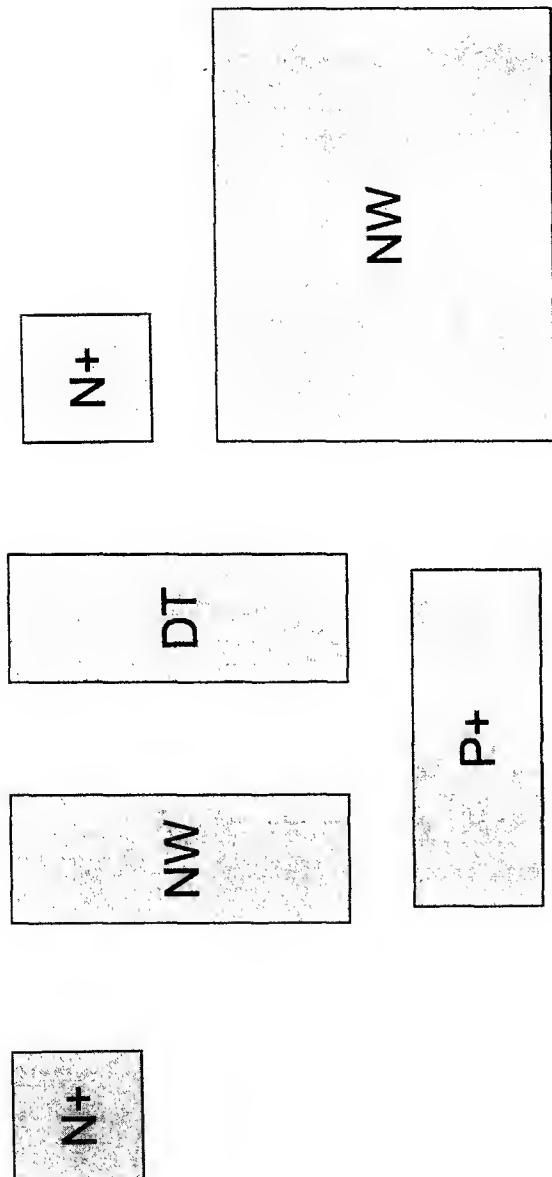


Figure 3

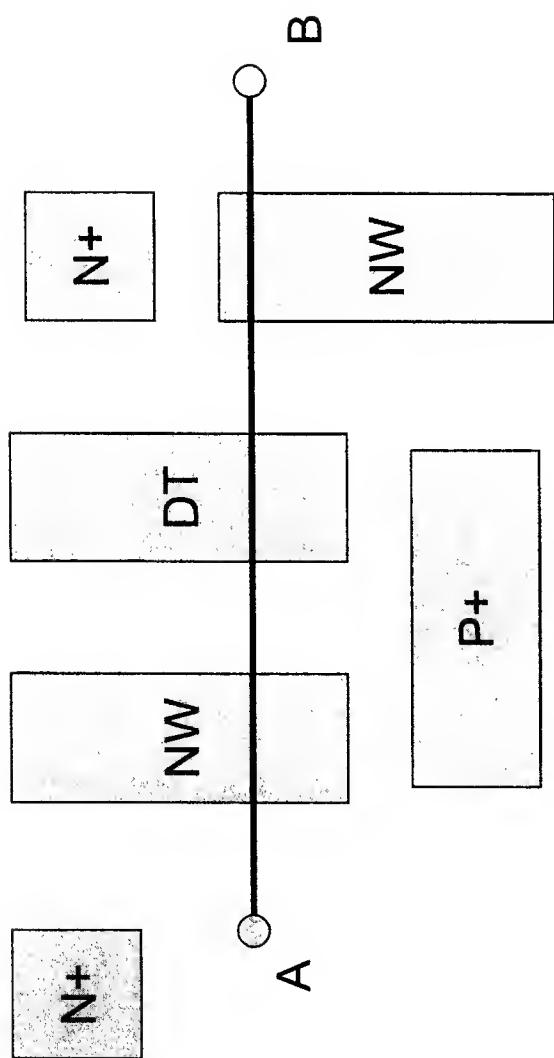


Figure 4

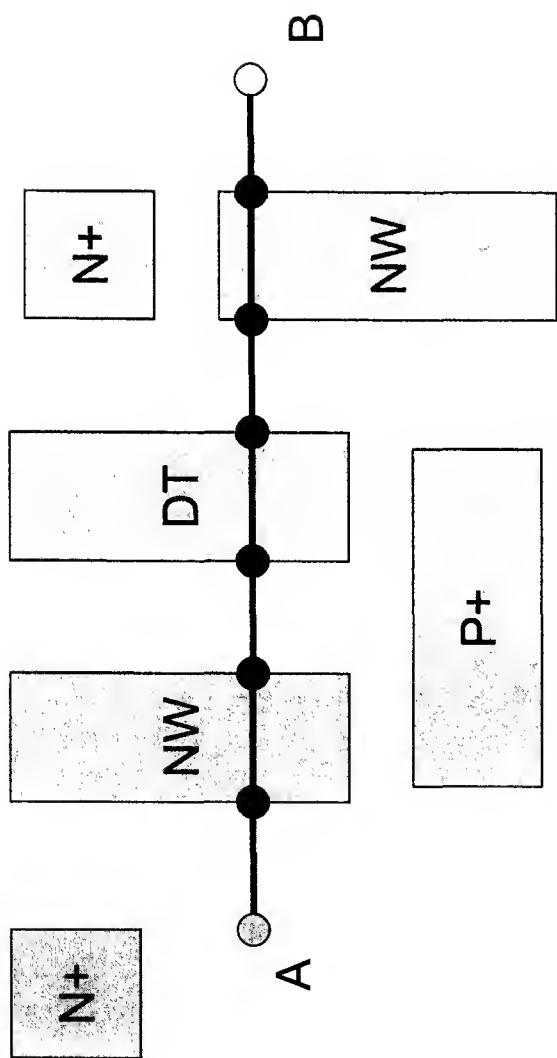
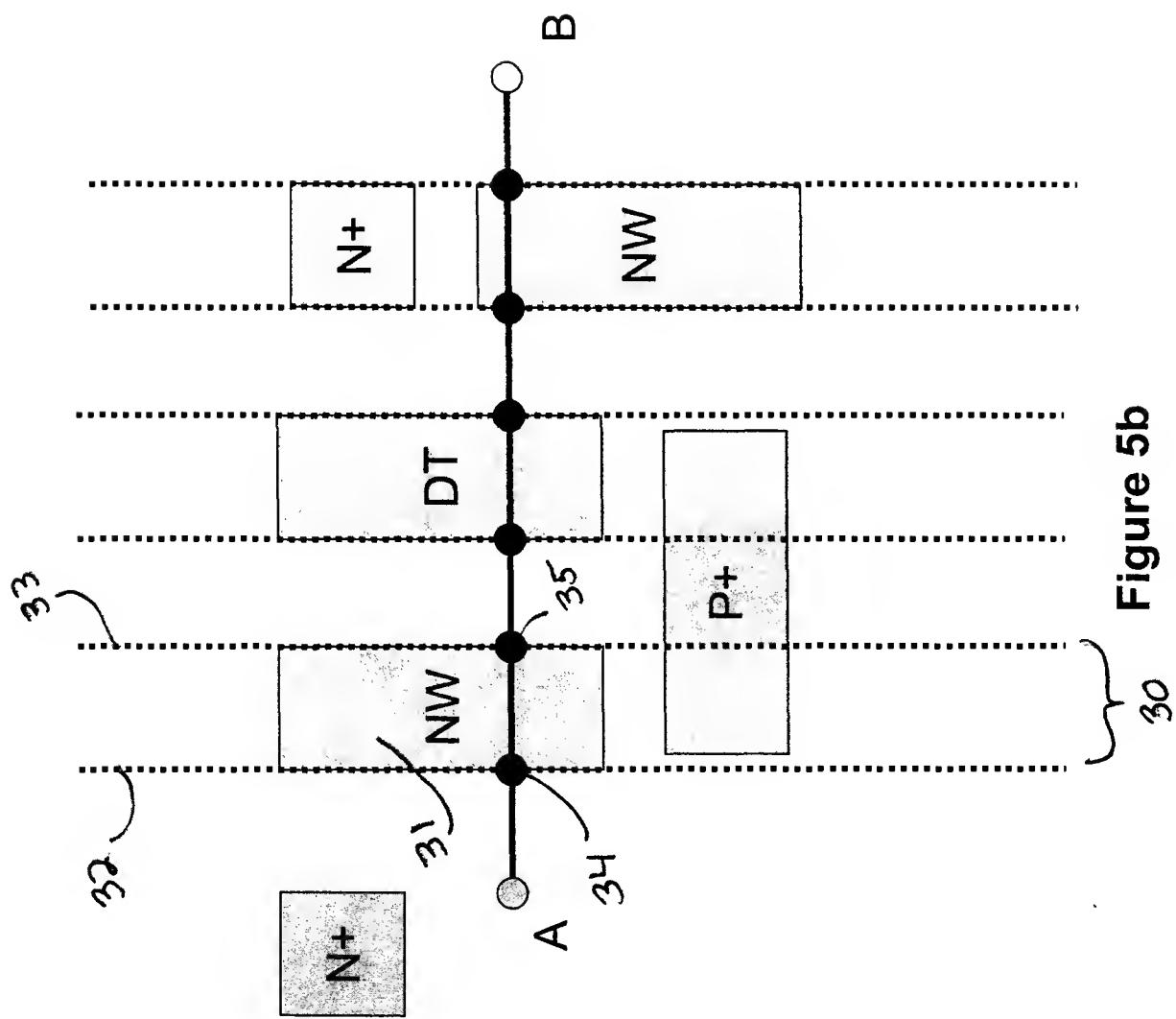
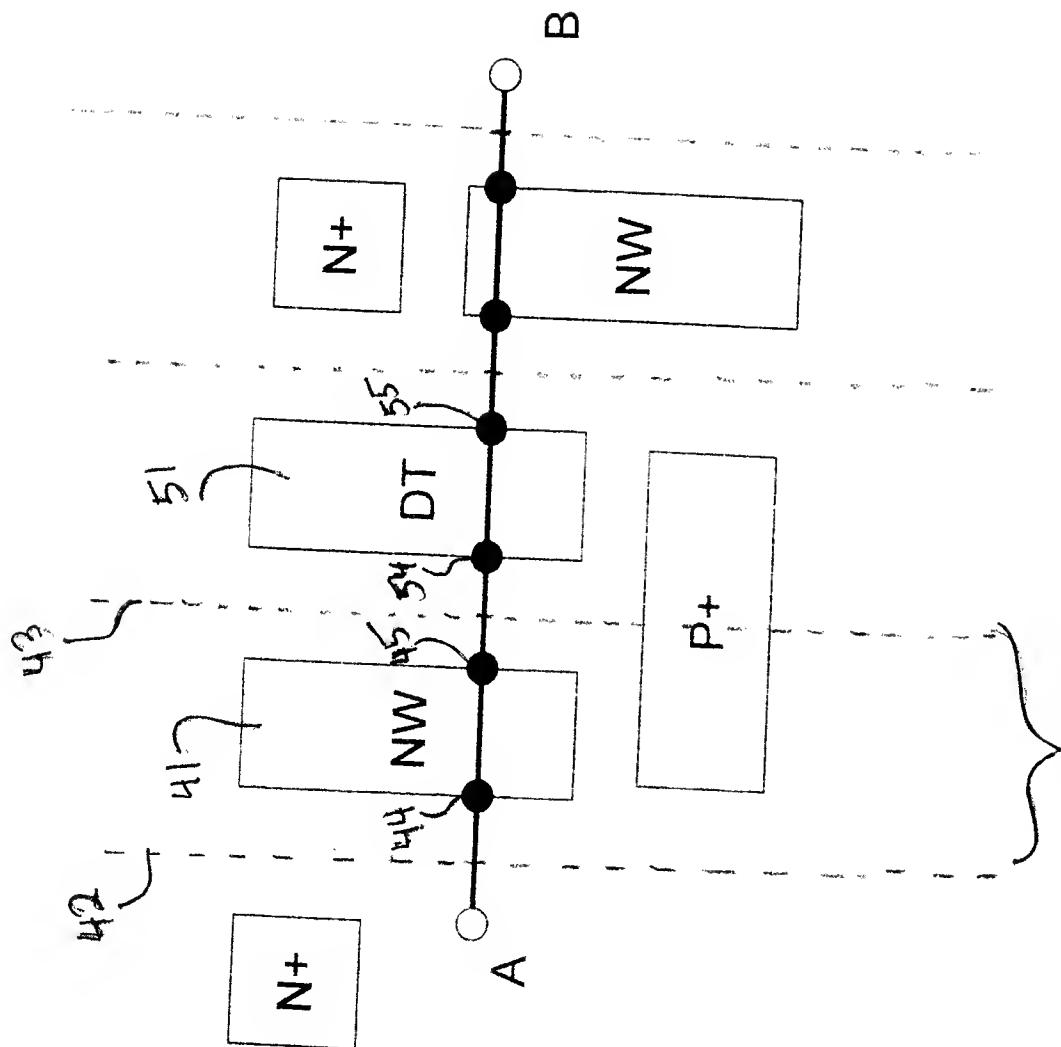


Figure 5a





40 Figure 5c

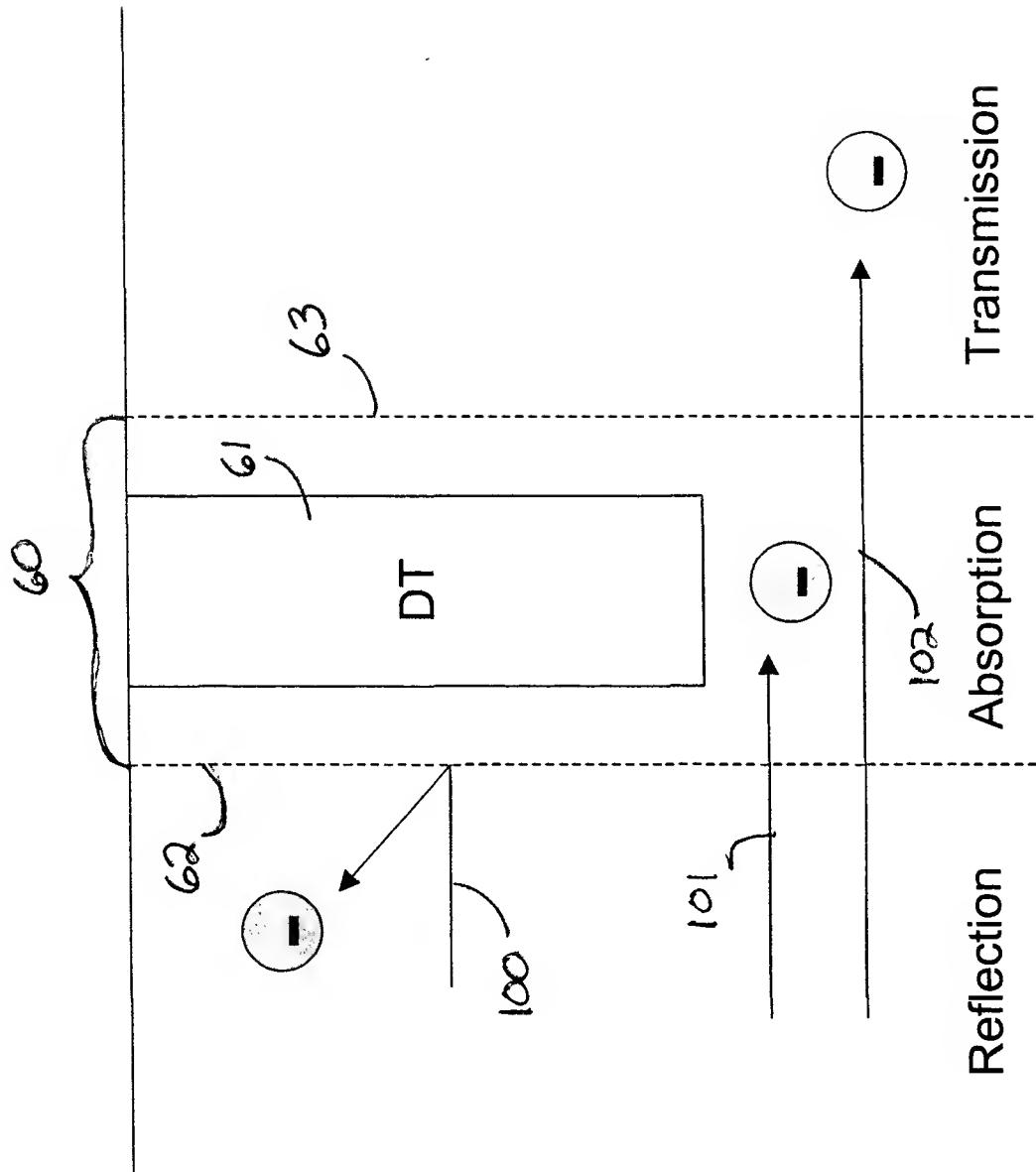


Figure 6a

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BUR920040120US1

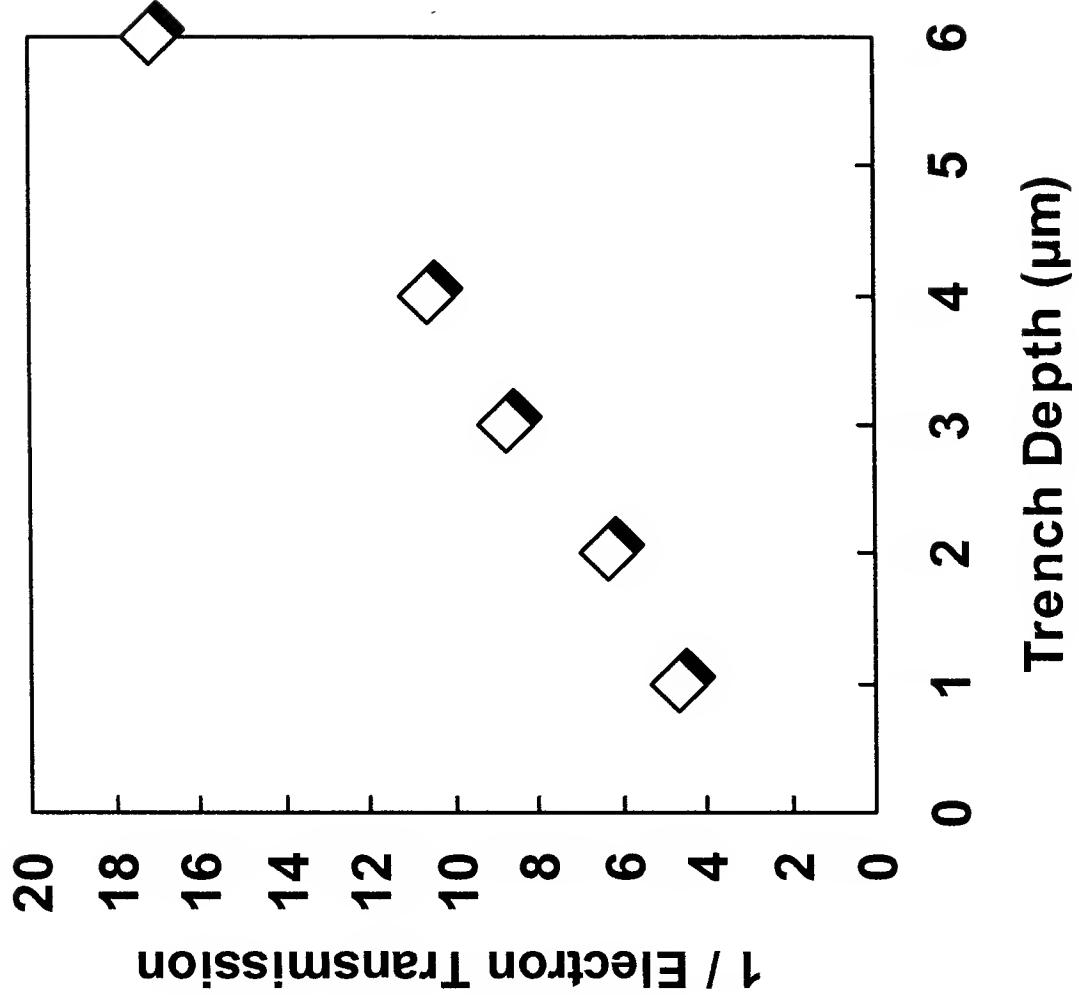


Figure 6b

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BUR920040120US1

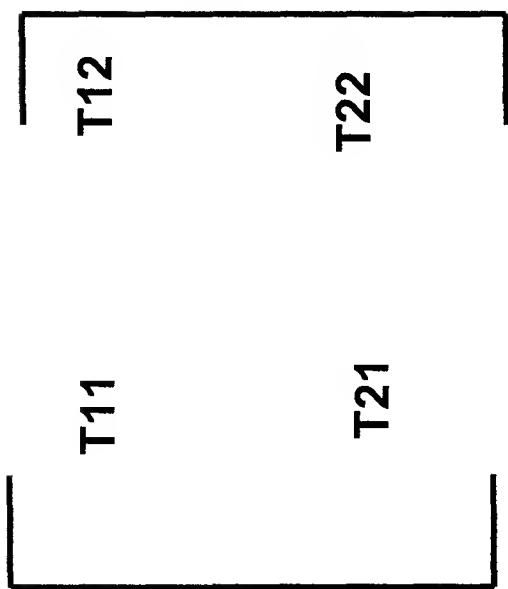


Figure 6c

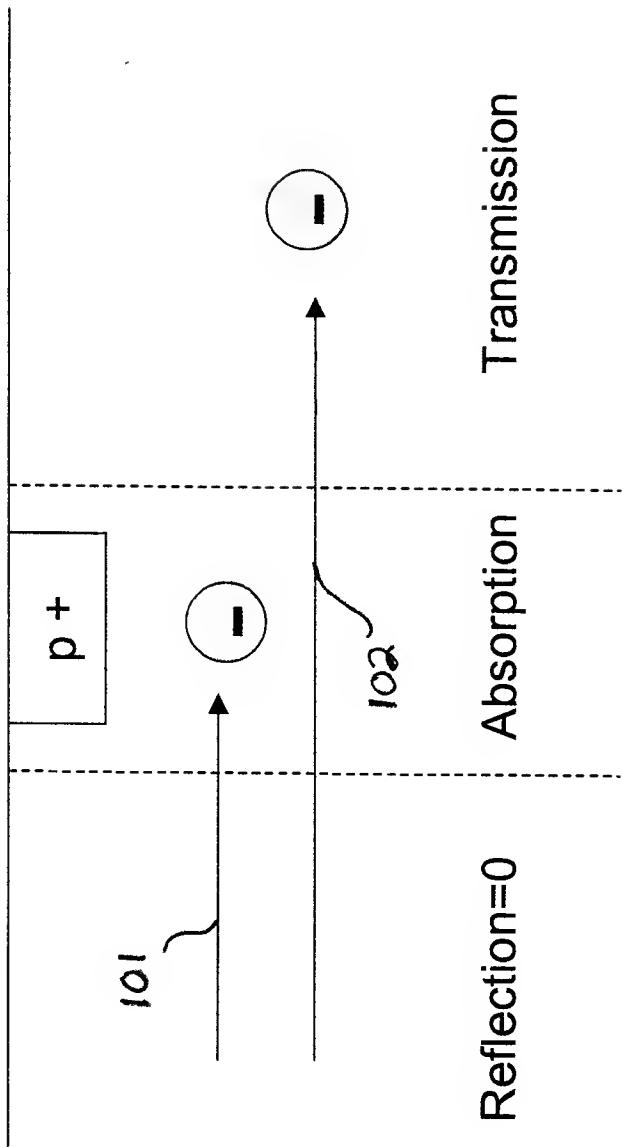


Figure 7

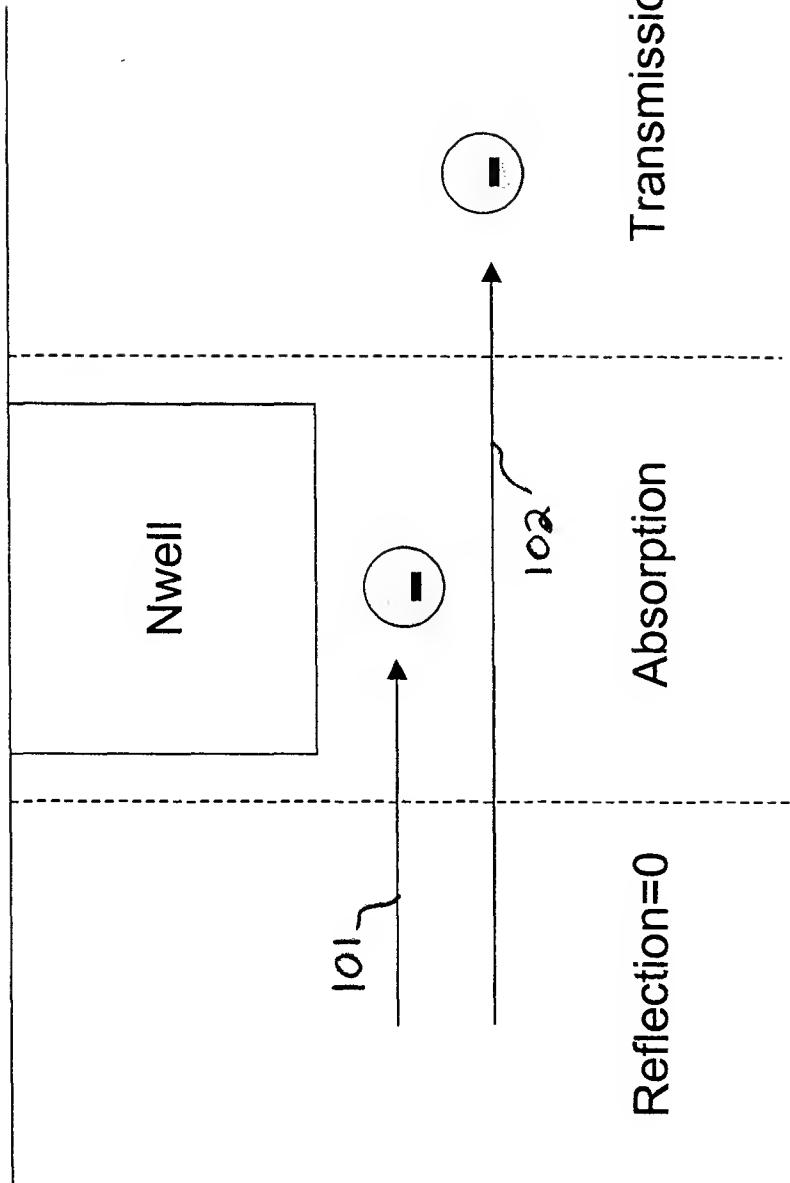


Figure 8

UNIT - CELL

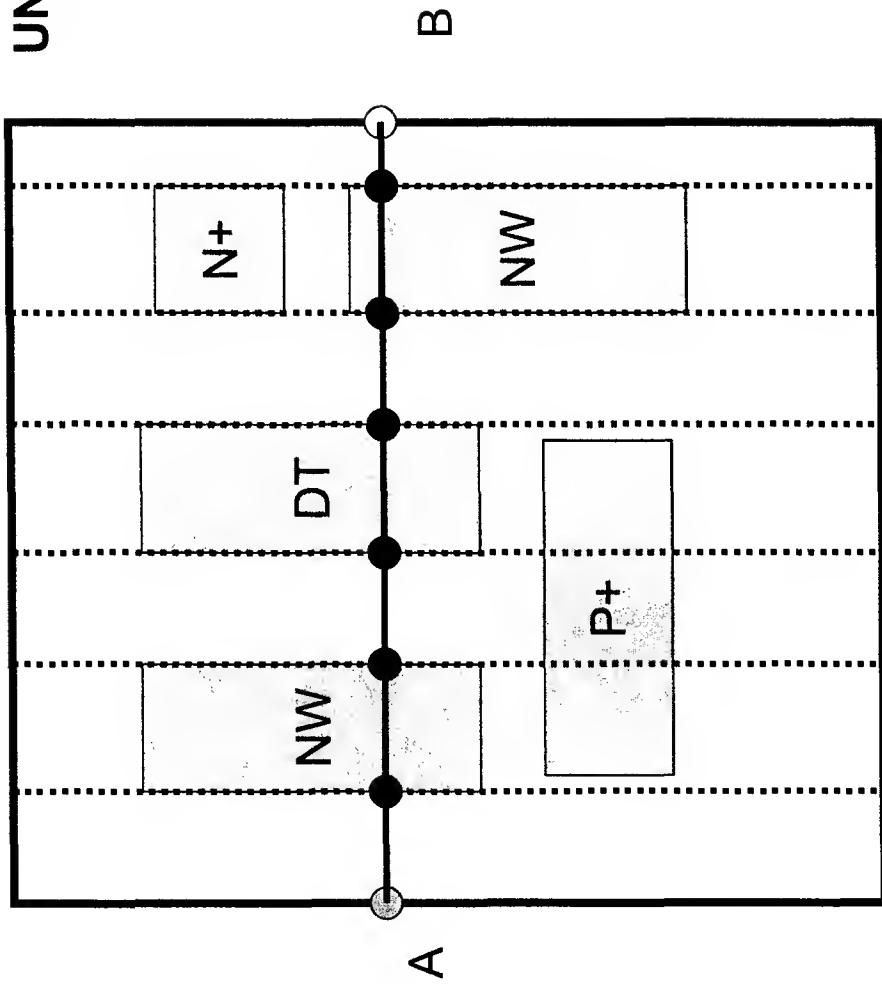
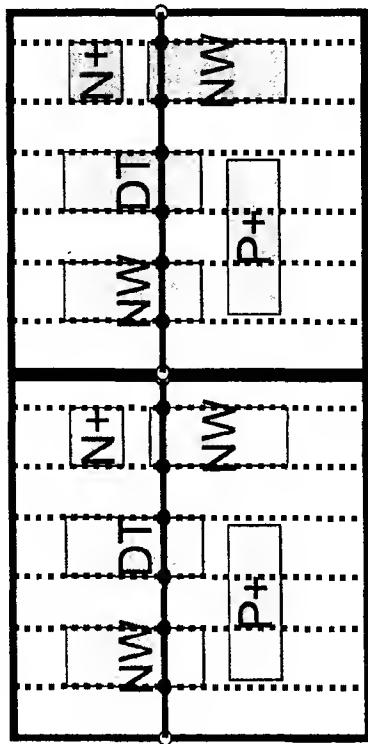


Figure 9

ARRAY

UNIT CELL



...

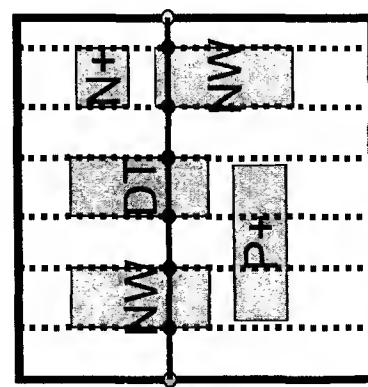


Figure 10

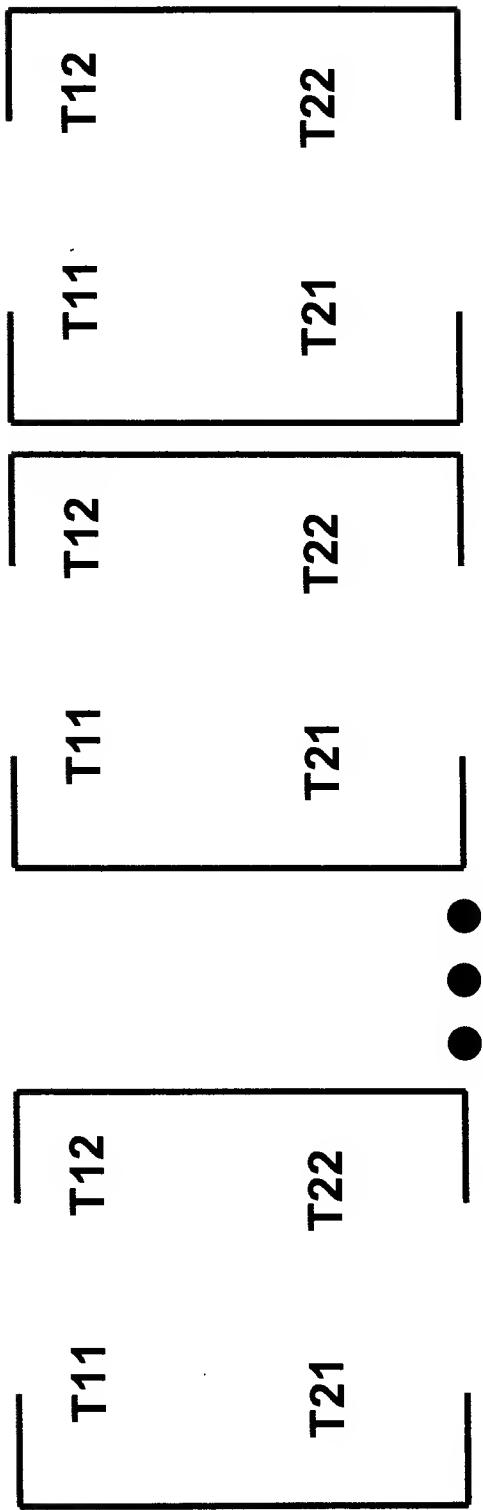


Figure 11 N- Unit Cells

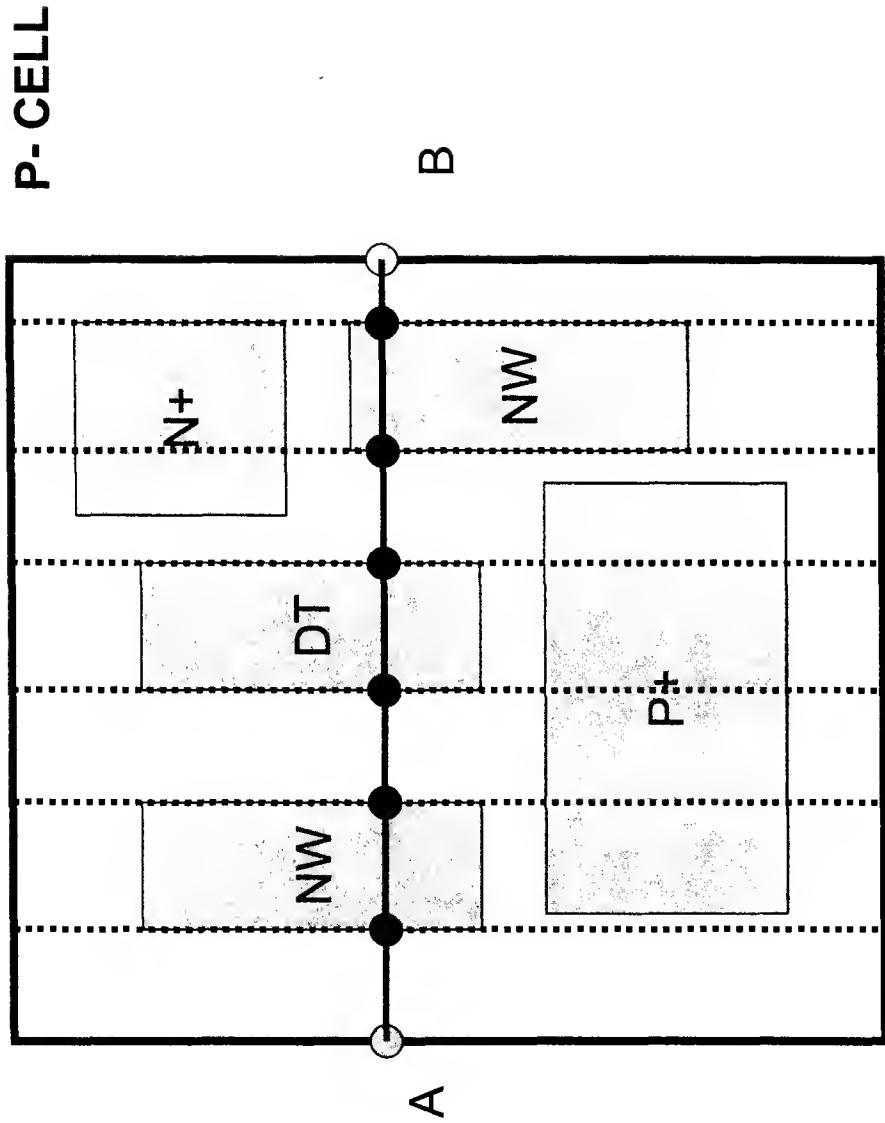


Figure 12

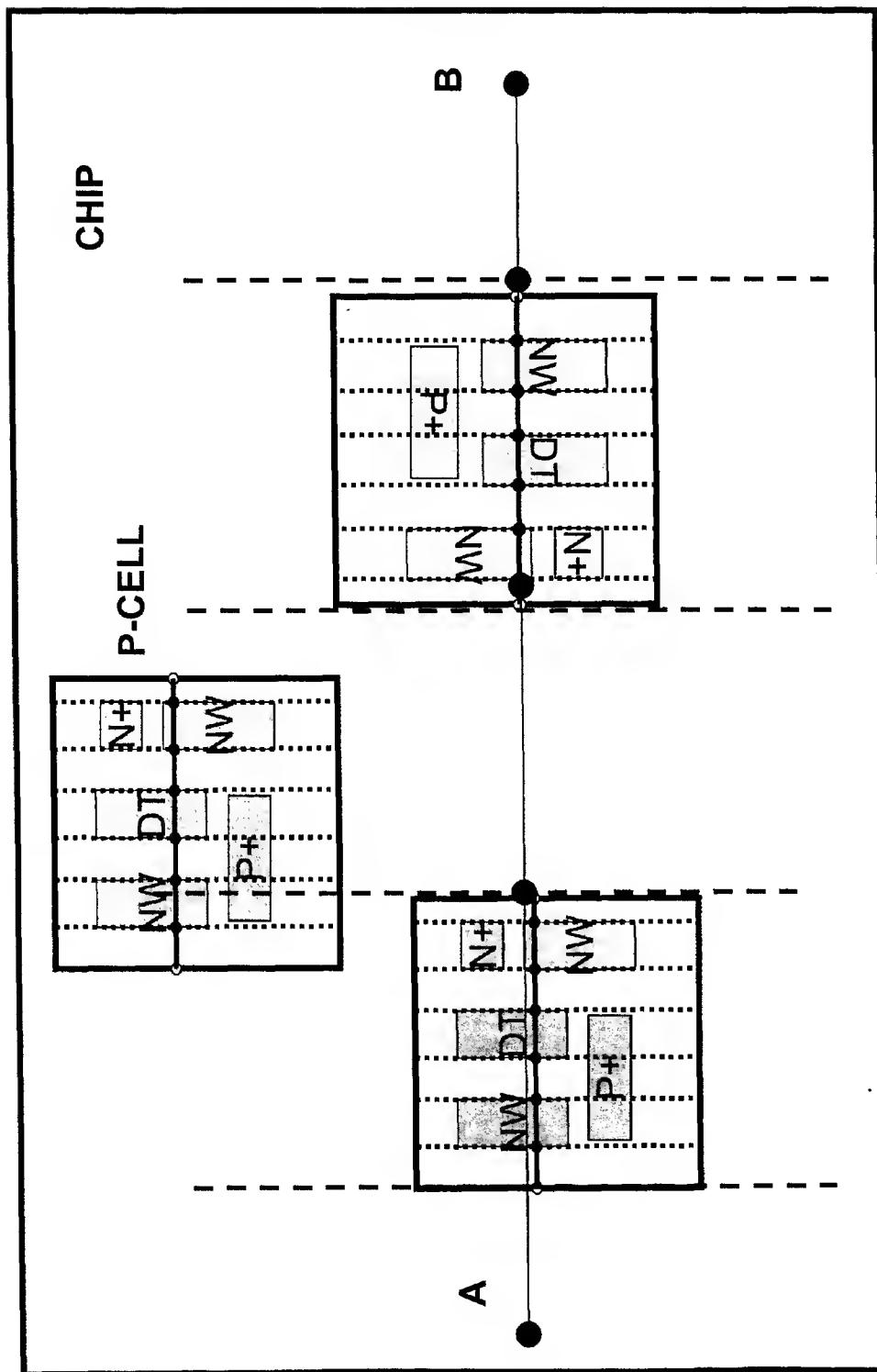


Figure 13

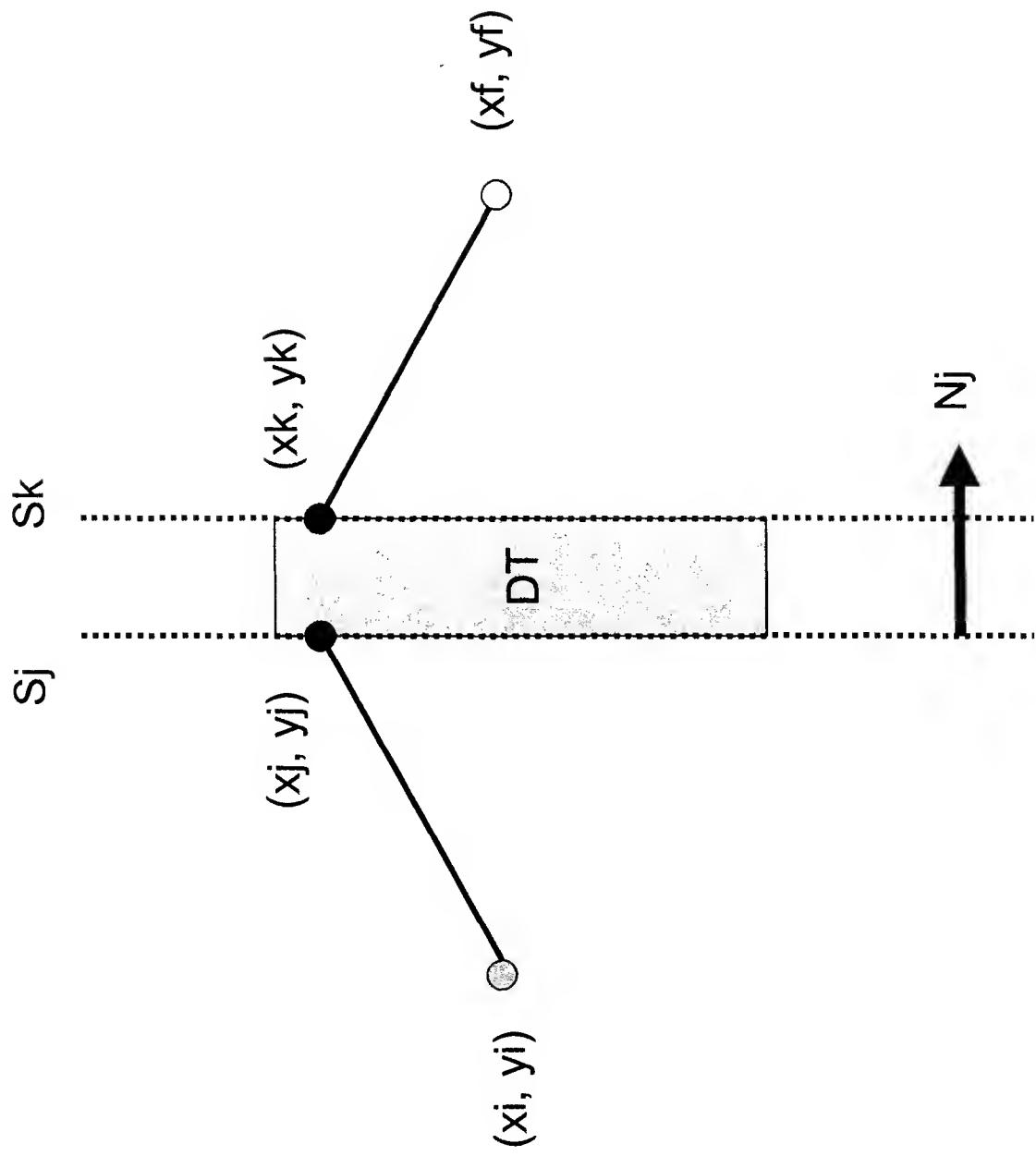


Figure 14

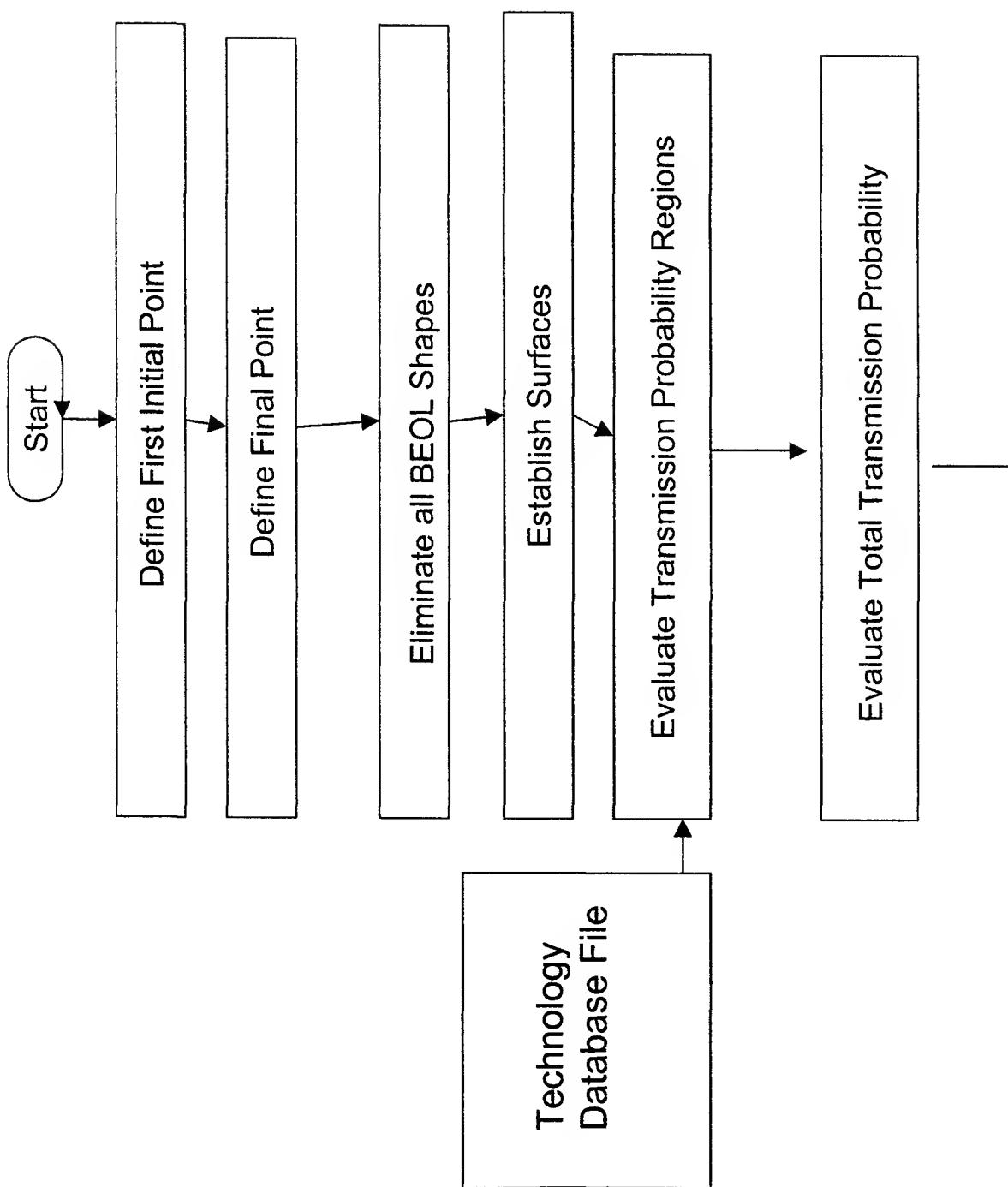


Figure 15a. Evaluation of the Transmission Probability

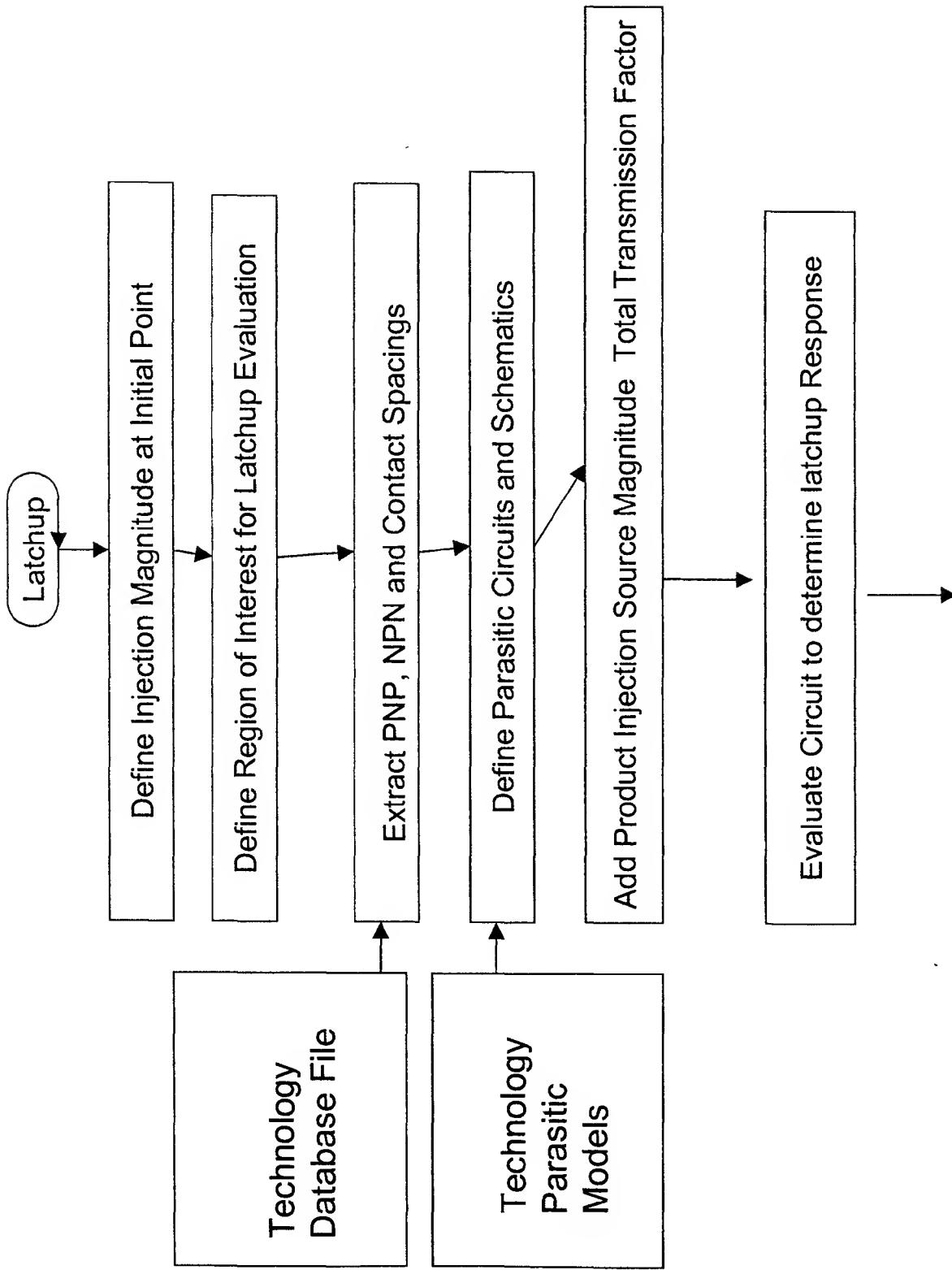


Figure 15 (b) Evaluation of Latchup using Parasitic Circuit Models

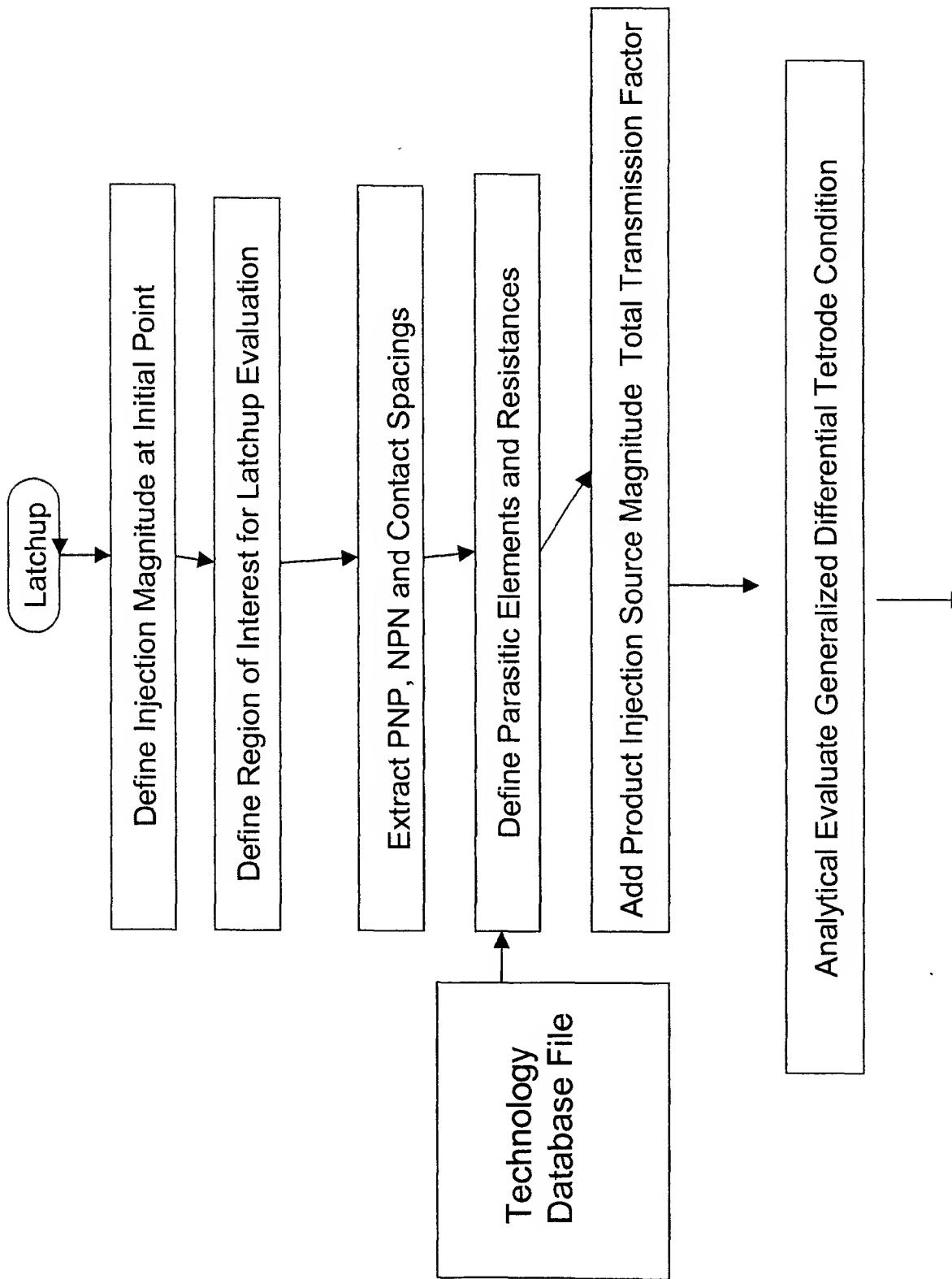


Figure 15 (c) Evaluation of Latchup Using Analytical Model

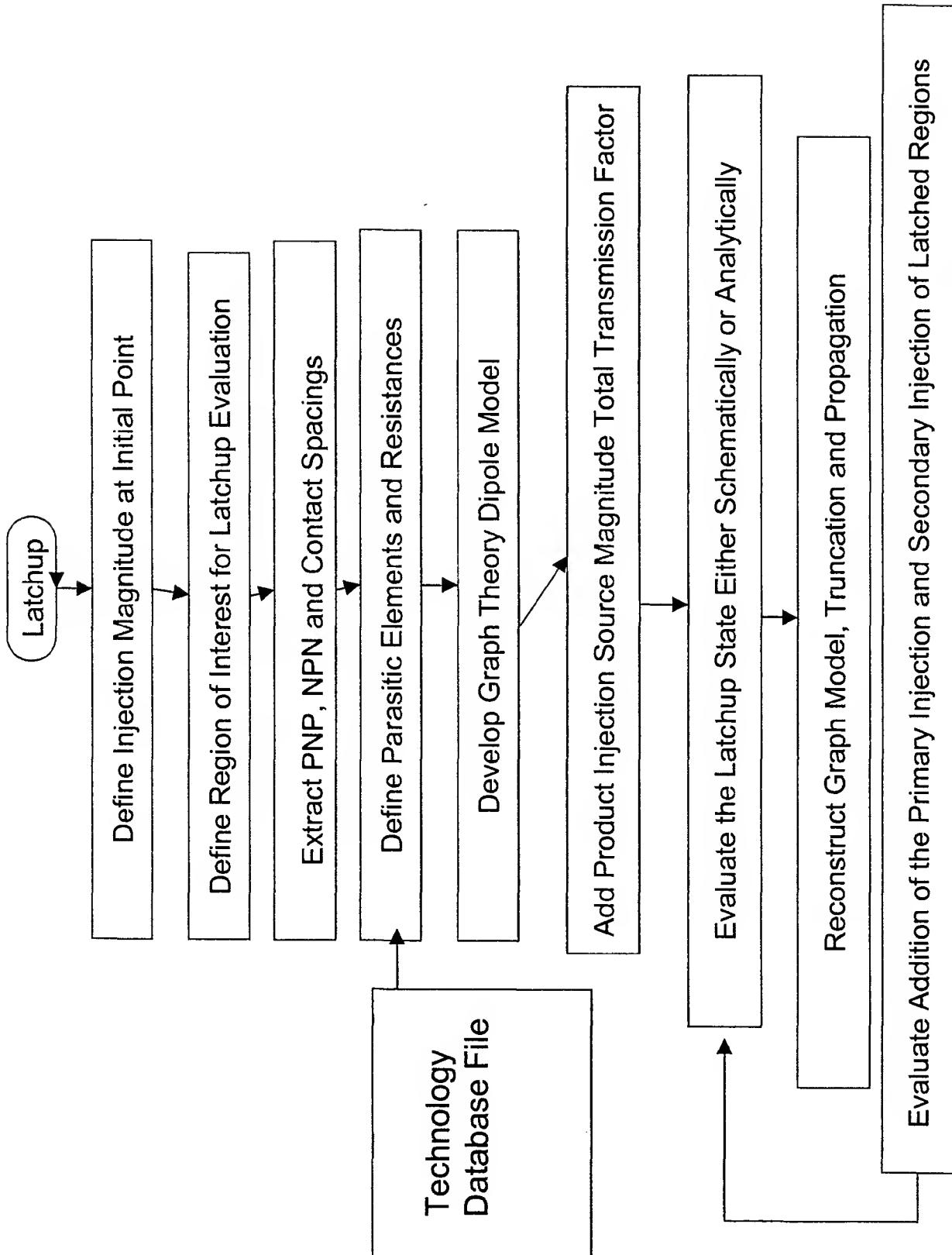


Figure 15 (d) Evaluation of Latchup Using Analytical Model

